Blakely, Sokoloff, Taylor & Zafman LLP Title: HETEROSTRUCTURE BIPOLAR TRANSISTOR

1st Named Inventor: Yasuhiro ODA Express Mail No.: EV665782387US

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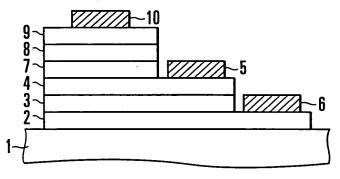


FIG. 1

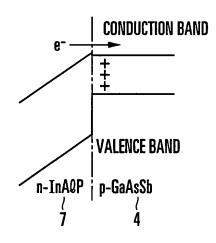


FIG. 2

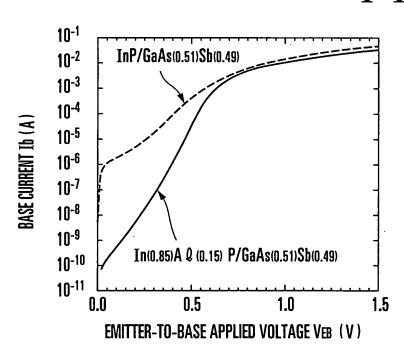


FIG. 3

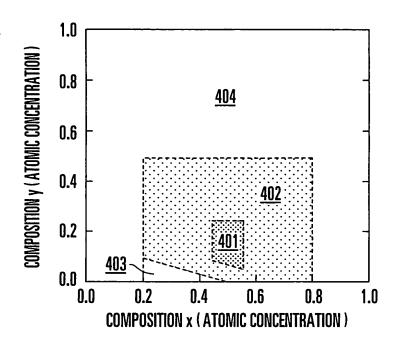
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F I G. 4

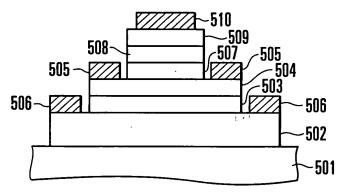


FIG.5A

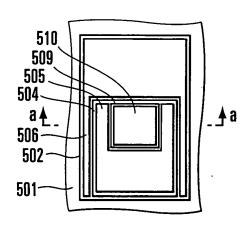


FIG.5B

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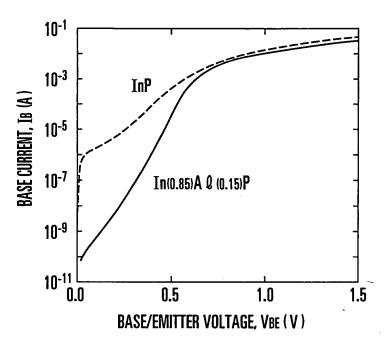


FIG. 6

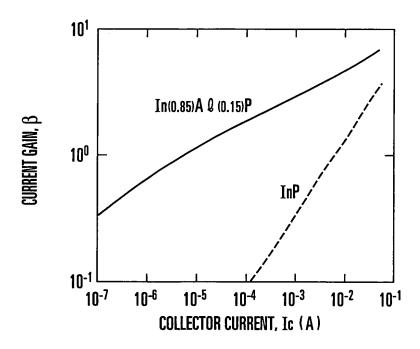


FIG. 7

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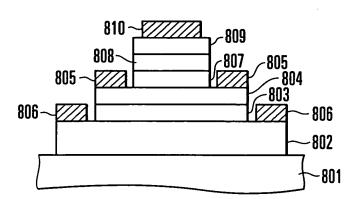
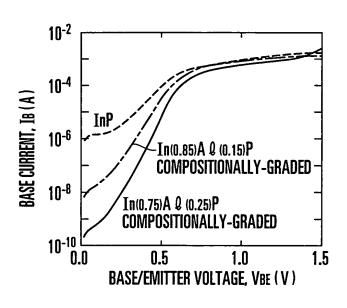


FIG. 8



F I G. 9

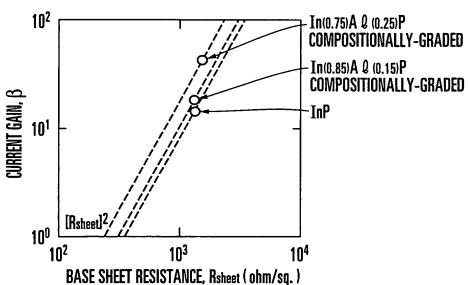


FIG. 10

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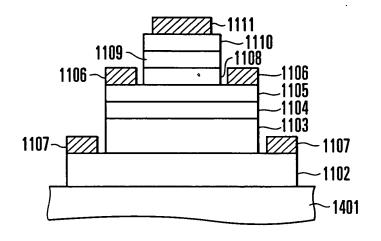


FIG. 11

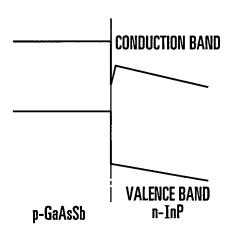


FIG. 12

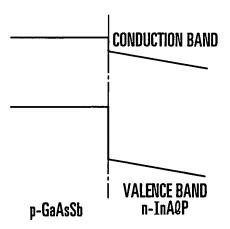


FIG. 13

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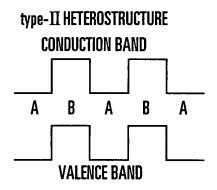
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type-I HETEROSTRUCTURE

**CONDUCTION BAND** B B A **VALENCE BAND** 

FIG. 14



## FIG. 15

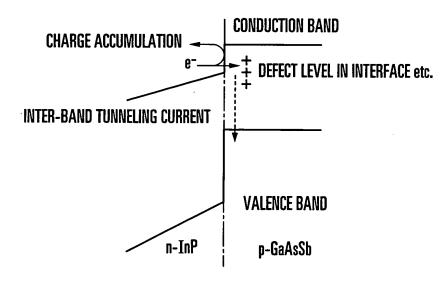


FIG. 16